

1/3/02

**INFORMATION DISCLOSURE
CITATION FORM FOR
PATENT APPLICATION
(FORM PTO-1449)
(Substitute)**

Docket No.: 872.0043.USU

Serial No.: 09/891,726

Applicant(s): Heikkila

Filing Date: 6/26/01

Group: 2661

U.S. PATENTS

Initials	Patent Number	Issue Date	Name	Class	Sub-class	Filing date
					Technology Center 2600	RECEIVED JAN 11 2002

FOREIGN PATENT DOCUMENTS

Initials	Document Number	Date	Country	Name	Translation? Yes/No/n/a

OTHER DOCUMENTS (Title, Author, Date, Pages, Etc., if known)

DXN	"ASMD With Duty Cycle Correction Scheme For High-Speed DRAM"; Jang, S. et al; Electronic Letters; Vol. 37, Issue 16; August 2, 2001; pp. 1004-1006
DXN	"The Anti Jitter Circuit for the Suppression of Wideband Phase-Noise"; Underhill, M.J.; IEEE Colloquium on Microwave and Millimeter-Wave Oscillators and Mixers (Ref. No. 1998/480); December 1, 1998; pp. 2/1-2/14
DXN	"Sampling Jitter in High-Speed SI Circuits.pdf"; Jonsson, B.E.; IEEE International Symposium on Circuits and Systems; Vol. 1; 1998; pp. 524-526
DXN	"Effect Of Sample Clock Jitter On IF-Sampling IS-95 Receivers.pdf"; Stewart, K.A.; The 8 th IEEE International Symposium on Personal, Indoor and Mobile Radio Communications; Vol. 2; 1997; pp. 366-370
DXN	"Design Of High-Speed, Low Power Frequency Dividers And Phase-Locked loops in Deep Submicron CMOS"; Razavi, B. et al; IEEE Journal of Solid State Ciruits; Vol. 30, No. 2; February, 1995; pp.101-109

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